[Information Theory & Coding Techniques](https://www.youtube.com/playlist?list=PLLvKknWU7N4yDkIlN4YE-sXfFD4trDf6W)

<https://www.youtube.com/watch?v=sc9grztQClc&list=PLLvKknWU7N4yDkIlN4YE-sXfFD4trDf6W&index=3>

[Information Theory and Coding(ITC) Lectures in](https://www.youtube.com/playlist?list=PLV8vIYTIdSnaigcBvSa_S1NVdHhJHz9a4)

<https://www.youtube.com/watch?v=vKoo0HLLE50&index=33&list=PLV8vIYTIdSnaigcBvSa_S1NVdHhJHz9a4>

Mix - Cmos basics

<https://www.youtube.com/watch?v=1LRRUtgsM2s&index=10&list=RDQMCpf2oE0nT0U>

[Analog CMOS Design](https://www.youtube.com/playlist?list=PL9CC669A6A8D4D488)

<https://www.youtube.com/watch?v=dKNzHqLEtYM&list=PL9CC669A6A8D4D488>

[LDPC](https://www.youtube.com/playlist?list=PLvOt25oj1u_uIP9mWPDem48ebMM5YuMV0)

<https://www.youtube.com/watch?v=ZFE5Vj-C83o&list=PLvOt25oj1u_uIP9mWPDem48ebMM5YuMV0>

Linear Block codes part1

https://www.youtube.com/watch?v=L\_1xsIqYaAc

### [4.1– DIGITAL ELECTRONICS IN HINDI | SWITCHING THEORY IN HINDI | LOGIC | TUTORIALS | LECTURES | GATE | NET | PSU | KNOWLEDGE GATE](https://www.youtube.com/playlist?list=PLmXKhU9FNesSfX1PVt4VGm-wbIKfemUWK)

[KNOWLEDGE GATE](https://www.youtube.com/channel/UCA6yfpYhy5sWMjRGOT-OAIQ)

<https://www.youtube.com/watch?v=ztkMRln_jDA&list=PLmXKhU9FNesSfX1PVt4VGm-wbIKfemUWK>

[Distributed System Tutorial In Hindi](https://www.youtube.com/playlist?list=PL-JvKqQx2AtdBuhyvazfDL-sIwbA7iTbr)

<https://www.youtube.com/watch?v=18i50-qZT60&list=PL-JvKqQx2AtdBuhyvazfDL-sIwbA7iTbr>

[Digital 2 with PLD](https://www.youtube.com/playlist?list=PLX7ZPgPJWZNcDCIwqswjjaafqb3U2sVCb)

<https://www.youtube.com/watch?v=XG2mQDi9tW4&list=PLX7ZPgPJWZNcDCIwqswjjaafqb3U2sVCb&index=12>

[Digital Electronics](https://www.youtube.com/playlist?list=PLBlnK6fEyqRjMH3mWf6kwqiTbT798eAOm)

<https://www.youtube.com/watch?v=V-e9Q9YWC6U&list=PLBlnK6fEyqRjMH3mWf6kwqiTbT798eAOm&index=53>

# **Mod-02 Lec-07 Wafer fabrication, inspection and testing**

<https://www.youtube.com/watch?v=Tacv0fA9n_s>

### [From Truth Table to DNF or CNF](https://www.youtube.com/watch?v=tpdDlsg4Cws)

<https://www.youtube.com/watch?v=tpdDlsg4Cws>

# **Scan Chain based Sequential Circuit Testing**

<https://www.youtube.com/watch?v=UQMQ6BtDP6Y>

[VLSI Physical Design NPTEL](https://www.youtube.com/playlist?list=PLCmoXVuSEVHlEJi3SwdyJ4EICffuyqpjk)

https://www.youtube.com/watch?v=lRpt1fCHd8Y&list=PLCmoXVuSEVHlEJi3SwdyJ4EICffuyqpjk

# **lecture 33 - Signature Analysis & Built In Self Test (BIST)**

<https://www.youtube.com/watch?v=bkA9FHQuzVU>

### [Digital Hardware Design by Prof.M.Balakrishnan](https://www.youtube.com/playlist?list=PLBCE46DD08E83F210)

[Satish Kashyap](https://www.youtube.com/channel/UCVkGOMGLPJZ4-k2ES43N0iA)

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<https://www.youtube.com/watch?v=WJUTZpmlixE&list=PLBCE46DD08E83F210>

# **16 2 MemTest march (update 2016.9.12)**

<https://www.youtube.com/watch?v=q5UnuEuaNa4>

# **Lec-39 introduction to fpga**

<https://www.youtube.com/watch?v=CLUoWkJUnN0>

# **skl-31 Semiconductor Memories**

<https://www.youtube.com/watch?v=omxQsvenf9o>

# **What is a Zener Diode? - Electronics Basics 15**

<https://www.youtube.com/watch?v=XhQqtdTlRus>

# **Concept of Sinking and Sourcing in PLC | Steps towards learning Automation - 03**

<https://www.youtube.com/watch?v=6sQt1N0nBeE>

# **L46223 - NPN Sinking and PNP Sourcing Transistors**

<https://www.youtube.com/watch?v=LoBhLSpJzcY>

Hamming Code For Error Detection and Correction - Error Correcting Code

https://www.youtube.com/watch?v=m2dFjkZVlVw

Shift register circuit for a cyclic encode 2 - LFSR

<https://www.youtube.com/watch?v=5i7ucO8T_kw>

Encoding a circuit code

<https://www.youtube.com/watch?v=JQ6Ad4fCGIo>

L-97 Cyclic Encoder Designing with Example in Digital Communication by Engineering Funda

<https://www.youtube.com/watch?v=F8kIEAn6XWk>

L-109 Channel Capacity by Shannon - Hartley and Proof of channel Capacity by Shannon - Hartley

<https://www.youtube.com/watch?v=3ekWsXeZ8TM>

L-110 Example on Channel Capacity by Shannon - Hartley in Digital Communication by Engineering Funda

<https://www.youtube.com/watch?v=uhpu02mvHDY>

# **Mod-01 Lec-13 Implementing Fault Tolerance in Physical Architecture**

<https://www.youtube.com/watch?v=efrKfmokQGE>

3 4 FaultModeling DelayFault

<https://www.youtube.com/watch?v=nr254LAkFiY>

11 2 Delay Test, DFT - Test Mode Operation (LOS/LOC)

<https://www.youtube.com/watch?v=Km3WCLu4THA>

Mod-03 Lec-20 Delay modelling

<https://www.youtube.com/watch?v=i2dOOWnfjqk>

VLSI Design [Module 04 - Lecture 18] VLSI Testing: High-level fault modeling and RTL level Testing

<https://www.youtube.com/watch?v=cw2DstvTeCY>

D-Algorithm for ATPG

<https://www.youtube.com/watch?v=CC4vzaAUMWU>

D Algorithm

https://www.youtube.com/watch?v=mVoxhnDWM\_w